

ANALYSIS ON (15,7) BINARY BCH ENCODER AND DECODER FOR 7-BIT ASCII CHARACTERS

P.Mozhiarasi*

C.Gayathri**

Abstract-In digital communication, the data is transmitted through the channel to the receiver side. If noise is included in the channel, data that is received at the receiver side will not be the actual data. This may create a corruption of data which is intolerable. In such situations, an error control must be employed so that errors may be detected and afterwards corrected. In this paper, (15,7,2) BCH encoder and decoder are designed mathematically. The block length(n), data length(k) and maximum number of correctable errors(t) are 15, 7 and 2 respectively. The encoding and decoding operates over the Galois Field GF (2^4) with an irreducible polynomial x^4 +x+1. Decoder includes 3 major steps: 1) Syndrome calculation (SC) 2) Berlekamp Massey Algorithm (BMA) 3) Chein Search (CS). The error is then corrected effectively in decoder part with the help of NAND Flash memory.

Keywords: Bose Chaudhuri Hocquenghem (BCH), BCH Encoder, BCH Decoder, Berlekamp Massey Algorithm (BMA), Chein Search(CS), Galois Field (GF), Syndrome Calculation(SC).

*ME Student VLSI Design, Sri Eshwar College of Engineering, Coimbatore, Tamil Nadu, India **Asst. Prof Department of ECE, Sri Eshwar College of Engineering, Coimbatore, Tamil Nadu, India



1. INTRODUCTION:

Millions of tiny devices are combined to form the computer memory, each of which can store a 0 or a 1 by magnetically on a disk or as state in a transistor, a core or a vacuum tube. The word "bit" is used to refer to one of these individual atoms of memory. Bits are combined to form some meaningful data. When eight bits are grouped together they are represented as a byte. Using blocks of bits the computer memory can store numbers, text and even pictures. Bits in a byte may be normally numbered from zero to seven, where bit 0 indicates lower order bit or Least Significant Bit (LSB) and bit 7 indicates higher order bit or Most Significant Bit (MSB). All the other bits are referred by their respective number. When the data is transmitted in a noisy channel, it is difficult to retrieve actual data at receiver side. A single error may shutdown the whole processing unit and result in an unbearable loss of data. There are many different coding schemes [1], each having some particular advantages and characteristics. The more sophisticated error correcting code [5] is Bose, Chaudhuri and Hocquenghem (BCH) codes which are overview of the Hamming codes.

BCH code is a general algorithm to correct a small bit error with wide range of applications in digital communication s and storage [2]. They include BCH encoder and decoder with three processing stages 1) Syndrome calculation (SC) 2) Berlekamp Massey Algorithm (BMA) 3) Chein Search (CS). At last, certain errors are corrected automatically and enable reconstruction of the original data.

2. BCH CODES

BCH codes [3] are multiple random error-correcting codes which were discovered by A. Hocquenghem in 1959. It was continued by R. C. Bose and D. K. Ray Chaudhuri in 1960. BCH codes are known as cyclic codes which means that for any cyclic shift, the example of codeword is a valid codeword. BCH codes were also called as primitive BCH codes. As the BCH code operate in Galois Field, it can be defined by two parameters that are length of code words (*n*)and the number of error to be corrected *t*. BCH encoder and decoder block diagram is shown in Fig. 1. Fundamentally, BCH codes are defined by

Length of codeword, $n = 2^{m} - 1$

Number of parity check bits, $n-k \le mt$

Minimum distance, $d_{min} \ge 2t+1$

where,

m – the degree or integer of the generating polynomial



k – length of information

t – maximum number of correctable errors



Figure 1 Block Diagram of BCH Encoder and Decoder

Consider a text message "VLSI" should be sent through the channel without any interruption. But it is not possible to receive the actual data when they are passed through noisy channel. Each character is sent one by one with the help of (Linear Feedback Shift Register) LFSR in a network. The system to encode letters and characters into numbers is called the ASCII code (American Standard for Information Interchange). The transmission on computer and radio networks uses binary symbols. By the American Standard Code for Information Interchange the letter "V" is defined by a binary value of 1010110. Similarly, for L,S, I represents 1001100, 1010011 and 1001001 respectively. A sentence is then represented by a string of these values: "VLSI" = 1010110, 1001100, 1010011, 1001001. To detect errors in the transmission of such a string of ones and zeros, then it is better to make a coding system that works with binary symbols.

Assume that 7 bit ASCII character is transmitted through noisy channel there is a chance of error occurrence. In order to detect the error in communication append them with eight zeros before transmitting [6]. This process occurs in encoder part, where Combination of message bit and redundant bit is called codeword. At the receiver side, decoder includes three important steps to detect and correct the bits if error is present.

| Power | Polynomial | 4-Tuple | Decimal | Minimal | |
|------------------|------------------------------------|---|---------|--|--|
| Form | Form | Form | Form | polynomial | |
| | | α ³ , α ² , α, 1 | | | |
| (° | 0 | 0000 | 0 | x | |
| 1, α^{13} | 1 | 0001 | 1 | x+1 | |
| ζa | a | 0010 | 2 | $x^{4}+x+1$ (f ₁ (x)) | |
| α ² | α² | 0100 | 4 | $x^4 + x + 1$ $(f_2(x))$ | |
| α | a | 1000 | 8 | $x^4 + x^3 + x^2 + x + I(f_3(x))$ | |
| a | a+1 | 0011 | 3 | $x^4 + x + 1$ $(f_4(x))$ | |
| ď | α ² + α | 0110 | 6 | $x^2 + x + 1$ $(f_5(x))$ | |
| as | $\alpha^3 + \alpha^2$ | 1100 | 12 | $x^4 + x^3 + x^2 + x + I(f_{\sigma}(x))$ | |
| α | $\alpha^3 + \alpha + 1$ | 1011 | 11 | x ⁴ +x ³ +1 | |
| as | $\alpha^2 + 1$ | 0101 | 5 | x*+x+1 | |
| a | $\alpha^3 + \alpha$ | 1010 | 10 | $x^4 + x^3 + x^2 + x + 1$ | |
| a10 | $\alpha^2 + \alpha + 1$ | 0111 | 7 | x ² +x+1 | |
| an | $\alpha^3 + \alpha^2 + \alpha$ | 1110 | 14 | x ⁴ +x ³ +1 | |
| a ¹² | $\alpha^3 + \alpha^2 + \alpha + 1$ | 1111 | 15 | $x^4 + x^3 + x^2 + x + 1$ | |
| a ¹³ | $\alpha^3 + \alpha^2 + 1$ | 1101 | 13 | x ⁴ +x ³ +1 | |
| α!4 | α ³ +1 | 1001 | 9 | x ⁴ +x ³ +1 | |

Table 1: *GF(16)* generated with Primitive polynomial x^4+x+1

Let us consider m=4, $n=2^m -1=2^4 -1=15$, k=7, t=2, $d_{min} = 2t+1=2(2)+1=5$. Let $f_i(x)$, i=1,2,3,...,2tbe the minimal polynomial, then $g(x) = LCM\{f_1(x), f_2(x), f_3(x), ..., f_{2t}(x)\}$. But, g(x) is simplified to $g(x) = LCM\{f_1(x), f_3(x), ..., f_{2t-1}(x)\}$ because every even power of primitive element will have same minimal polynomial as some odd power of the elements having the number of factors in the polynomial [7]. Therefore, $g(x) = LCM\{f_1(x), f_3(x)\} = x^8 + x^7 + x^6 + x^4 + 1 = 111010001$ where $f_1(x) = x^4 + x + 1 = 10011$ and $f_3(x) = x^4 + x^3 + x^2 + x + 1 = 11111$. These factors are obtained by primitive polynomial $x^4 + x + 1$ which are shown in Table 1 and conjugates of 16 field elements using Galois Field theory [8] are shown in Table 2.

| | Conjugate roots | Minimal polynomial |
|--------------------|---|-----------------------------------|
| | 0 | x |
| | 1 | x+1 |
| <i>φ</i> 1(x) | $\alpha, \alpha^2, \alpha^4, \alpha^8$ | x ⁴ +x+1 |
| φ ₂ (x) | $\alpha^3, \alpha^6, \alpha^9, \alpha^{12}$ | $x^4 + x^3 + x^2 + x + 1$ |
| φ3(x) | α^5, α^{10} | x^2+x+1 |
| φ4(x) | $\alpha^7, \alpha^{11}, \alpha^{13}, \alpha^{14}$ | x ⁴ +x ³ +1 |

Table 2 Minimal Polynomials of the element in *GF(16)*

Each character process is organized as follows:



2.1 Transmission of letter "V"

2.1.1 BCH Encoder

In initial stage, the message polynomial(M(x)) is padded with (*n*-*k*) zero bits [13] to form X^{n-k} M(x) i.e., X^{n-k} $M(x) = X^8$ M(x) = M + 0000000 = 101011000000000. The remainder polynomial(R(x)) is obtained by dividing the X^8 M(x) by g(x). Combining remainder polynomial with message polynomial produce the codeword (c(x)) [4]. i.e., $c(x) = X^{n-k} M(x)$ + R(x) = 101011001000111

2.1.2 BCH Decoder

2.1.2.1 Syndrome Calculation:

Let us consider c(x) with 2 errors (t = 2) and represent as r(x) i.e., when noise is added in the channel while transmitting the data to the receiver [10]. At the receiver side, let us assume r(x) = 100011001010111. The number of syndrome elements is 2*t = 4 them, to find t=2 errors. Those syndrome elements are represented as S_1 , S_2 , S_3 , S_4 and they can be calculated by

$$S_{1}(\alpha) = r(\alpha) \mod f_{1}(\alpha) = \alpha^{3} + \alpha^{2} = \alpha^{6}$$

$$S_{2}(\alpha^{2}) = r(\alpha^{2}) \mod f_{2}(\alpha^{2}) = r(\alpha^{2}) \mod f_{1}(\alpha^{2}) = (\alpha^{2})^{3} + (\alpha^{2})^{2} = \alpha^{12}$$

$$S_{3}(\alpha^{3}) = r(\alpha^{3}) \mod f_{3}(\alpha^{3}) = (\alpha^{3})^{3} + (\alpha^{3}) + 1 = \alpha^{4}$$

$$S_{4}(\alpha^{4}) = r(\alpha^{4}) \mod f_{4}(\alpha^{4}) = r(\alpha^{4}) \mod f_{1}(\alpha^{4}) = (\alpha^{4})^{3} + (\alpha^{4})^{2} = \alpha^{9}$$

By the property of Field element several powers of generating element will have the same minimal polynomial. When f(x) is polynomial over GF(2), α is an element of $GF(2^m)$. If the remainder equals zero, then it is declared that no error in the codeword or else error(*S*) is present [9]. So this error will be preceded for finding error location.

2.1.2.2 Berlekamp Massey Algorithm:

This method has been reduced by a Berlekamp algorithm [11] and error locator polynomial is obtained by iterative method. d_{μ} represents discrepancy value in Lin and Costello table as shown in Table 3.

Table 3 Finding error locator polynomial

| μ | σ ^(μ) (x) | dμ | lμ | 2μ- l _μ |
|------|-----------------------------------|----------------|----|--------------------|
| -1/2 | 1 | 1 | 0 | -1 |
| 0 | 1 | α ⁶ | 0 | 0 |
| 1 | $\alpha^{6}x+1$ | α7 | 1 | 1 |
| 2 | $\alpha x^{2} + \alpha^{6} x + 1$ | - | - | - |

The "Key Equation" is given by



$$\begin{aligned} \sigma^{(\mu+1)}(x) &= \sigma^{\mu}(x) + d_{\mu} d_{\rho}^{-1} x^{2(\mu-\rho)} \sigma^{(\rho)}(x) & ------\text{equation 1} \\ l_{\mu+1} &= L = deg \mathbb{E}[\sigma^{(\mu+1)}(x)) & ------\text{equation 2} \\ d_{\mu+1} &= S_{2\mu+3} + \sigma_1^{(\mu+1)} S_{2\mu+2} + \sigma_2^{(\mu+1)} S_{2\mu+1} + \cdots + \sigma_L^{(\mu+1)} S_{2\mu+3-L} ------\text{equation 3} \\ \\ \text{Steps:} \\ (i) \quad \text{Initialize } \mu = 0, d_{\mu} \neq 0, \rho = -1/2 \\ (ii) \quad \text{Substitute} \quad \sigma^{(\mu)}(x) &= \sigma^{(0)}(x) = 1, \quad d_{\mu} = d_0 = S_1 = \alpha^6 \quad , \quad d_{\rho}^{-1} = d_{-\frac{1}{2}}^{-1} = 1, \\ \sigma^{(\rho)}(x) &= \sigma^{\left(-\frac{1}{2}\right)}(x) = 1 \text{ in equation 1} \\ \sigma^{(1)}(x) &= \sigma^0(x) + d_0 d_{-1/2}^{-1} x^{2\left(\frac{1}{2}\right)} \sigma^{\left(-\frac{1}{2}\right)}(x) \\ &= 1 + \alpha^6(1)^{-1} x(1) \\ &= \alpha^6 x + 1 \\ (iii) \quad \text{Substitute } \sigma^{(1)}(x) = \alpha^6 x + 1 \text{ in equation 2} \\ &l_1 = \deg(\sigma^{(1)}(x)) = \deg(\alpha^6 x + 1) = 1 \\ (iv) \quad \text{Substitute } S_3 = \alpha^4, S_2 = \alpha^{12}, \sigma_1^{(1)} = \alpha^6 \text{ in equation 3} \\ &d_1 = S_3 + \sigma_1^{(1)} S_2 \\ &= \alpha^4 + (\alpha^6) (\alpha^{12}) \\ d_1 = \alpha^7 \\ (v) \quad \text{In similar way, consider } \mu = 1, d_{\mu} \neq 0, \rho = 0 \text{ and substitute} \end{aligned}$$

 $\sigma^{(\mu)}(x) = \sigma^{(1)}(x) = \alpha^{6}x + 1, d_{\mu} = d_{1} = \alpha^{7}, d_{\rho}^{-1} = d_{0}^{-1} = (\alpha 6) - 1, \sigma^{(\rho)}(x) = \sigma^{(0)}(x) = 1$ in equation 1 $\sigma^{(2)}(x) = \sigma^{1}(x) + d_{1} d_{0}^{-1} x^{2(1)} \sigma^{(0)}(x)$ $= (\alpha 6x + 1) + (\alpha 7)(\alpha 6) - 1 x 2(1)$

$$\sigma^{(2)}(x) = \alpha x^2 + \alpha^6 x + 1$$

The last polynomial represents the final error locator polynomial.

2.1.2.3 Chien Search Algorithm:

The roots of $\sigma^{(\mu)}(x)$ in GF (2⁴) should be found out by following order [12]. If $\sigma^{(\mu)}(x) = \sigma^{(2)}(x) = \alpha x^2 + \alpha^6 x + 1 = 0$ is obtained by substituting x = 0,1, α , α^2 , α^3 , ..., α^{14} then they are considered as roots.

Example:
$$\sigma^{(2)}(0) = \alpha (0)^2 + \alpha^6(0) + 1 = 1 \neq 0$$

 $\sigma^{(2)}(1) = \alpha (1)^2 + \alpha^6(1) + 1 = \alpha + (\alpha^3 + \alpha^2) + 1 \neq 0$

...



$$\begin{aligned} \sigma^{(2)}(\alpha^3) &= \alpha \,(\alpha^3)^2 + \alpha^6(\alpha^3) + 1 \\ &= \alpha^7 + \alpha^9 + 1 \\ &= (\alpha^3 + \alpha + 1) + (\alpha^3 + \alpha) + 1 \\ &= 0 \\ & \dots \\ \\ \sigma^{(2)}(\alpha^{11}) &= \alpha \,(\alpha^{11})^2 + \alpha^6(\alpha^{11}) + 1 \\ &= \alpha^{23} + \alpha^{17} + 1 \\ &= (\alpha^2 + 1) + (\alpha^2) + 1 \\ &= 0 \end{aligned}$$

Therefore, α^3 , α^{11} are the roots for $\sigma^{(2)}(x) = \alpha x^2 + \alpha^6 x + 1$. The bit position of error location will be the inverse of their roots (α^3 , α^{11}) i.e., 001000000010000

Thus, the error pattern polynomial can be written as $e(x) = x^{12}+x^4$.

c(x) = r(x) + e(x)= 100011001010111 + 00100000010000 = <u>101011001000111</u> _{M(x)} R(x)

As a result, the actual data is recovered by performing modulo-2 addition for r(x) and e(x).

| | L | S | I | |
|--|--|---|--|--|
| Binary value | 1001100 | 1010011 | 1001001 | |
| Remainder | 10011 | 1110000 | 100100 | |
| Codeword c(x) | 100110000010011 | 101001101110000 | 100100100100100 | |
| Received polynomial r(x) | 100111001010011 | 101001111111000 | 110101100100100 | |
| Syndrome Calculation(SC): Syndrome (S ₁ , S ₂ , S ₃ , S ₄) | $\alpha^5, \alpha^{10}, \alpha^{10}, \alpha^5$ | $\alpha^4, \alpha^8, \alpha^5, \alpha^2$ | $\alpha^{10}, \alpha^5, \alpha^5, \alpha^{10}$ | |
| Berlekamp Massey Algorithm(BMA): | $\begin{tabular}{ c c c c c c c c c c } \hline \mu & \sigma^{(\mu)}(x) & d_{\mu} & l_{\mu} & 2\mu \cdot l_{\mu} \\ \hline -1/2 & 1 & 1 & 0 & -1 \\ \hline 0 & 1 & \alpha^5 & 0 & 0 \\ \hline 1 & \alpha^5 x + 1 & \alpha^5 & 1 & 1 \\ \hline 2 & x^2 + \alpha^5 x + 1 & - & - & - \\ \hline \end{tabular}$ | $\begin{array}{ c c c c c c }\hline \mu & \sigma^{(a)}(\mathbf{x}) & \mathbf{d}_{\mu} & \mathbf{l}_{\mu} & 2\mu \cdot \mathbf{l}_{\mu} \\ \hline -1/2 & 1 & 1 & 0 & -1 \\\hline 0 & 1 & \alpha^4 & 0 & 0 \\\hline 1 & \alpha^4 \mathbf{x} + 1 & \alpha^{14} & 1 & 1 \\\hline 2 & \alpha^{10} \mathbf{x}^2 + \alpha^4 \mathbf{x} + 1 & - & - & - \\\hline \end{array}$ | $\begin{array}{ c c c c c c } \hline \mu & \sigma^{(a)}(\mathbf{x}) & \mathbf{d}_{\mu} & \mathbf{l}_{\mu} & 2\mu\text{-}\mathbf{l}_{\mu} \\ \hline -1/2 & 1 & 1 & 0 & -1 \\ \hline 0 & 1 & a^{15} & 0 & 0 \\ 1 & a^{15}\mathbf{x}\text{+}1 & a^2 & 1 & 1 \\ \hline 2 & a^{*}\mathbf{x}^{2}\text{+}a^{15}\mathbf{x}\text{+}1 & - & - & - \\ \hline \end{array}$ | |
| Final error locator polynomial ($\sigma^{(\mu)}(x)$) | $x^2 + \alpha^5 x + 1$ | $\alpha^{10}x^2 + \alpha^4x + 1$ | $\alpha^7 x^2 + \alpha^{10} x + 1$ | |
| Chein Search(CS): Roots Error pattern polynomial e(x) | $rac{lpha^6, lpha^9}{x^9+x^6}$ | $a^8 a^{12} a^{7} a^{7} a^{12}$ | a^{2}, a^{6} $x^{13}+x^{9}$ | |
| Recovered data $(c(x) = r(x) + e(x))$ | 100110000010011 | 101001101110000 | 100100100100100 | |

2.2 Transmission of letter "L", "S", "I"

From above table it is understood that the letters "L", "S", "I" which are received with errors at receiver part is detected by Syndrome Calculation (SC) and corrected with help of Berlekamp Massey Algorithm (BMA) and Chein Search (CS).

Thus, a text message "VLSI" that was sent through the channel reaches the other side of communication channel without any mismatch in text.



3. CONCLUSION:

Thus, by the use of 7 bit ASCII characters, the numbers, text and even pictures can be sent from one side to other side. While transmission is carried out, the actual data is recovered at the receiver side with the help of Berlekamp Massey Algorithm (BMA) and Chein Search (CS) even when the data is corrupted. Therefore, (15,7)BCH encoder and decoder [14] are derived mathematically which detects and corrects upto two errors in a efficient manner and implemented in future using hardware description language (HDL) known as Verilog, VHDL and synthesized by Xilinx ISE 13.2 simulator.

REFERENCES:

- [1] Berlekamp.E.R, "Algebraic Coding Theory", Mc GrawHill, New York, 1968.
- [2] Berlekamp, E.R., Peile, R.E. and Pope, S.P. (1987), "The application of error control to communications", IEEE Communication
- [3] Hank Wallace, "Error Detection and Correction using BCH Codes", Copyright © 2001 Hank Wallace.
- [4] <u>http://en.wikipedia.org/wiki/Polynomial long division</u>, an article on polynomial long division in Wikipedia.
- [5] Lin. S and Costello Jr. D.J, "Error Control Coding", Prentice-Hall, New Jersey, 1983.
- [6] M.Y. Rhee "Error Correcting Coding Theory", McGraw-Hill, Singapore, 1989.
- [7] P.Mozhiarasi , Ms.C.Gayathri, "A Mathematical Derivation for Error Correction and Detection in Communication using BCH Codes", IPASJ International Journal of Electronics & Communication, Vol. 2, Issue 10, p. 7-12, Oct. 2014.
- [8] R.E. Blahut, "Theory and practice of error-control codes", Addison-Wesley, Reading, MA, 1983.
- [9] Ryoh Fuji-Hara, "Galois Field Manual", University of Tsukuba, Second edition, June 2008.
- [10] S. B. Wicker, "Error Control Systems for Digital Communication and Storage". Upper Saddle River, New Jersey 075458: Prentice Hall, Inc1995.
- [11] Shu Lin, Daniel J. Castello, "Error control coding: Fundamentals and applications", Prentice-Hall, New Jersey, 1983.
- [12] Simon Haykin, "Communication Systems", 4th Edition, John Wiley & Sons, Inc.



- [13] W.W.Peterson, "Encoding and error-correction procedures for the Bose-Chaudhuri Codes", IRE Trans. Inf. Theory, September 1960.
- [14] Yunghsiang S. Han, "BCH Codes", Graduate Institute of Communication Engineering, National Taipei University Taiwan.

Authors



P. Mozhiarasi received her Bachelor's degree in Electronics and Communication Engineering from Dr. Mahalingam College of Engineering and Technology, Pollachi, Coimbatore District, Tamil Nadu in 2013 and pursuing Master's degree (VLSI Design) in Sri Eshwar College of Engineering,

Kinathukadavu, Coimbatore, Tamil Nadu.



Ms. C.Gayathri received her Bachelor's degree in Electronics and Communication Engineering from Sengunthar Engineering College, Tiruchengode in 2010 and Master's degree in Communication Engineering from Karpagam College of Engineering, Coimbatore in 2012. She joined as

an Assistant Professor, Electronics and Communication Engineering in Sri Eshwar College of Engineering, Kinathukadavu, Coimbatore, Tamil Nadu in 2012. She is well-versed in Computer Networks.